

- [Regan Robertson](#) Mod • [14 days ago](#)

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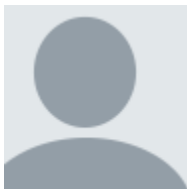
[Jian Huang](#) • [14 days ago](#)

Good morning! I'm happy to discuss our secure in-storage computing work with you!

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[Carl Nerup](#) • [14 days ago](#)

Good morning from Chicago. Best to you Dr. Huang.

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[Jian Huang](#) Carl Nerup · 14 days ago

Nice! We're so close. I'm from Urbana-Champaign.

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[Carl Nerup](#) Jian Huang · 14 days ago

Know it well. So glad to see the highly regarded University of Illinois having a leader on staff working virtualization.

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[Carl Nerup](#) · 14 days ago

Dr. Huang - we actually did a POC of this actual use case on Xilinx for a Government Customer. Would love to talk more about it with you to see our approach and how you might have done it differently or better.

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[Jian Huang](#) Carl Nerup • 14 days ago

Sounds good! I will be happy to learn more about your work and identify potential collaboration opportunities.

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[Jason H Li](#) • 14 days ago

I like the vision here. Would love to see more progress. seL4 for in-storage isolation sounds cool.

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[Jian Huang](#) Jason H Li • 14 days ago

Thank you, Jason!

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[Jason H Li](#) Jian Huang • 14 days ago

We should talk more. Let's sync up via emails etc.

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[Jian Huang](#) Jason H Li • 14 days ago

Definitely!

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[Regan Robertson](#) Mod • 14 days ago

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[Todd Carpenter](#) • 14 days ago

This is great work. I am interested in tracking your progress. What is the best way to do this?

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[Jian Huang](#) [Todd Carpenter](#) • 14 days ago

You can contact me via my email [jianh@illinois.edu](mailto:jianh@illinois.edu). We can schedule meetings to talk more. We will also post our progress on my website: <https://jianh.web.engr.illi...> :)

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[Nick Spinale](#) [Todd Carpenter](#) • 14 days ago • edited

I agree, this is fascinating! I am also interested in tracking your progress.

I'm also interested in learning more about the current state of this work. Are there any public materials you could point me to?

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[Jian Huang](#) [Nick Spinale](#) • [14 days ago](#)

Nick, thanks for your research interests! We're preparing the submission. I will be happy to share it with you when it's done.

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[Nick Spinale](#) [Jian Huang](#) • [14 days ago](#)

Thank you!

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[Jian Huang](#) Guest • 14 days ago

Lok, yes, we can. The reason why we don't do so is because this could introduce more performance overhead, so we currently rely on the address mapping table in the flash translation layer to interact with the flash chips inside SSD.

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